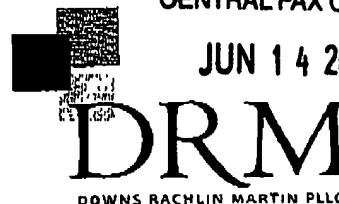


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BTV.255986.1

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Robert J. Allen et al.

Serial No.: 10/710,701

Filed: July 29, 2004

Title: Integrated Circuit Macro Placing  
System and Method

Group Art Unit: 2825

Examiner: Bihn C. Tat

Attorney Docket No.: BUR920040070US1  
(02016-00178)

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June 14, 2006

Response Transmittal

Transmitted herewith for filing is a Response to First Office Action with regard to the  
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Respectfully submitted,

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June 14, 2006

**RESPONSE TO FIRST OFFICE ACTION**

This is in response to the Office Action (paper no. 20060317) mailed from the United States Patent and Trademark Office on March 27, 2006, with respect to the above-identified application. A Status of the Claims and Remarks concerning the Office Action start, respectively, on pages 2 and 5.